TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

SSM3K329R

- Power Management Switch Applications
- High-Speed Switching Applications

• 1.8-V drive

• Low ON-resistance: $R_{DS(ON)}$ = 289 m Ω (max) (@V_{GS} = 1.8 V)

: $R_{DS(ON)} = 170 \text{ m}\Omega \text{ (max) (@V_{GS} = 2.5 V)}$

: $R_{DS(ON)}$ = 126 m Ω (max) (@V_{GS} = 4.0 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol		Rating	Unit	
Drain-source voltage		V_{DSS}		30	V	
Gate-source voltage		V_{GSS}		±12	V	
Drain current	DC	I _D (Note 1)		3.5	А	
	Pulse	I _{DP} (Note 1)		7.0		
Power dissipation		P _D (Note 2)		1	W	
			t = 10s	2	v v	
Channel temperature		T _{ch}		150	°C	
Storage temperature range		T _{stg}		-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: The channel temperature should not exceed 150°C during use.

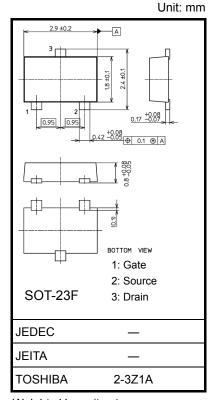
Note 2: Mounted on a FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ mm}, \text{Cu Pad: } 645 \text{ mm}^2)$

Handling Precaution

When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

Thermal resistance $R_{th (ch-a)}$ and Power dissipation P_D vary depending on board material, board area, board thickness and pad area. When using this device, please take heat dissipation into consideration



Weight: 11 mg (typ.)



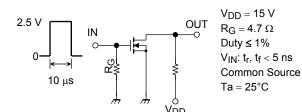
Electrical Characteristics (Ta = 25°C)

Chara	acteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit	
Drain-source breakdown voltage	V _{(BR) DSS}	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V		
Drain-Source breakdown voltage		V (BR) DSX	$I_D = 1 \text{ mA}, V_{GS} = -12 \text{ V}$	18	_		_	
Drain cut-off curre	nt	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$		_	_	1	μΑ
Gate leakage curr	ent	I _{GSS}	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0 \text{ V}$		_	_	±1	μΑ
Gate threshold vo	tage	V _{th}	$V_{DS} = 3 \text{ V}, I_D = 1 \text{ mA}$		0.4	_	1.0	V
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 3 V, I _D = 1.0 A	(Note 3)	2.1	4.2	_	S
	R _{DS} (ON)	I _D = 1.0 A, V _{GS} = 4.0 V	(Note 3)	_	96	126	mΩ	
Drain–source ON-resistance		I _D = 0.8 A, V _{GS} = 2.5 V	(Note 3)	_	118	170		
		I _D = 0.5 A, V _{GS} = 1.8 V	(Note 3)	_	158	289		
Input capacitance		C _{iss}			_	123	_	
Output capacitance		Coss	$V_{DS} = 15V, V_{GS} = 0 V, f = 1 N$	_	43	_	pF	
Reverse transfer of	capacitance	C _{rss}			_	18	_	
Total gate charge		Qg			_	1.5	_	
Gate-source charge		Q _{gs1}	V _{DS} = 15V, I _D = 2.0 A V _{GS} = 4 V		_	0.3	_	nC
Gate-drain charge		Q _{gd}			_	0.6	_	
Switching time	Turn-on time	t _{on}	V _{DD} = 15 V, I _D = 1.0 A,		_	9.2	_	ne
	Turn-off time	t _{off}	$V_{GS} = 0 \text{ to } 2.5 \text{ V}, R_G = 4.7 \Omega$		_	6.4	_	ns
Drain-source forward voltage		V _{DSF}	I _D = -3.5 A, V _{GS} = 0 V	(Note 3)	_	-0.90	-1.2	V

Note 3: Pulse test

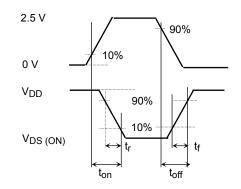
Switching Time Test Circuit

(a) Test Circuit



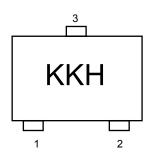
(b) V_{IN}

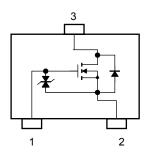
(c) V_{OUT}



Marking

Equivalent Circuit (top view)

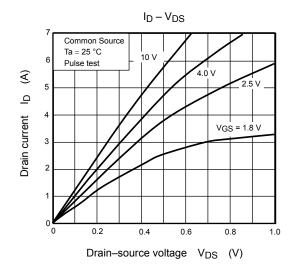


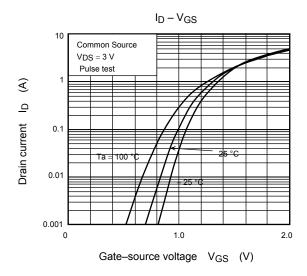


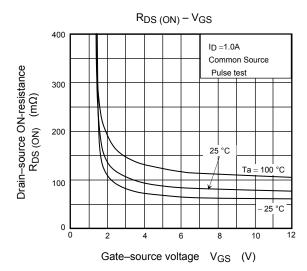
Usage Considerations

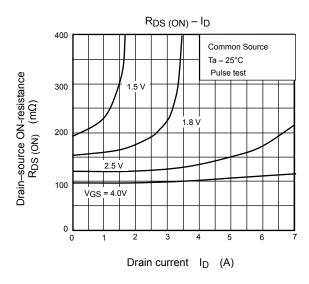
Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (1 mA for the SSM3K329R). Then, for normal switching operation, $V_{GS(on)}$ must be higher than V_{th} , and $V_{GS(off)}$ must be lower than V_{th} . This relationship can be expressed as: $V_{GS(off)} < V_{th} < V_{GS(on)}$.

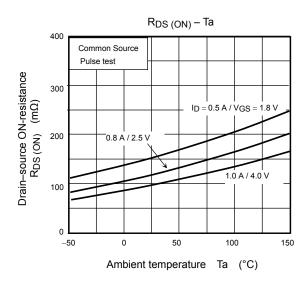
Take this into consideration when using the device.

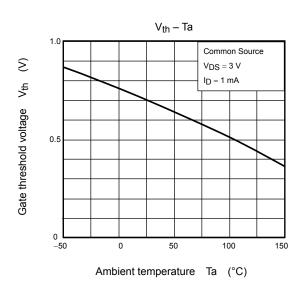


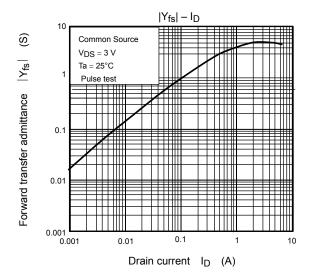


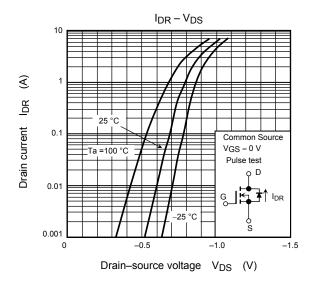


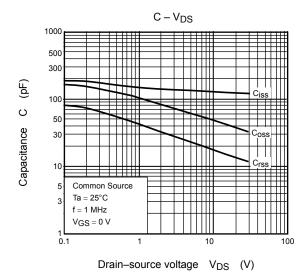


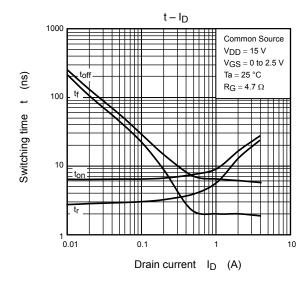


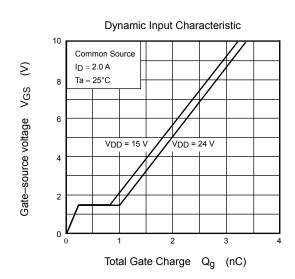




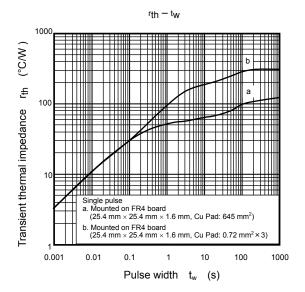


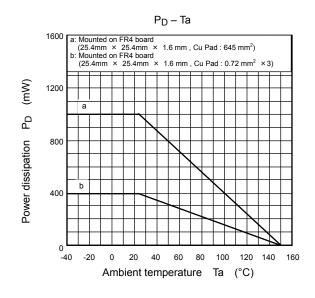






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